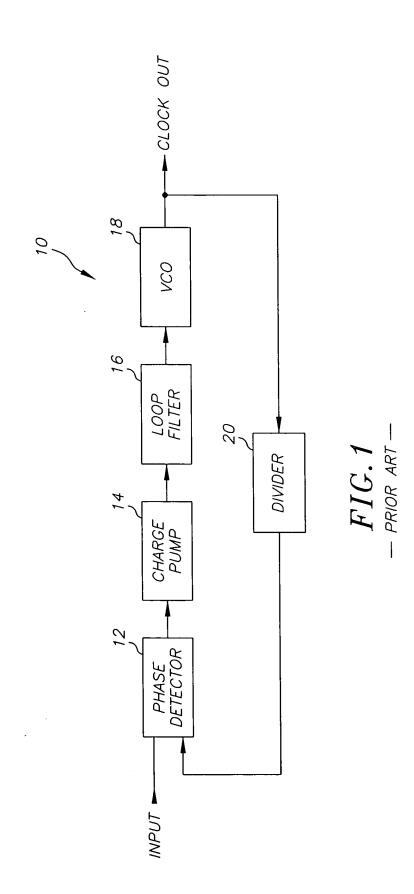
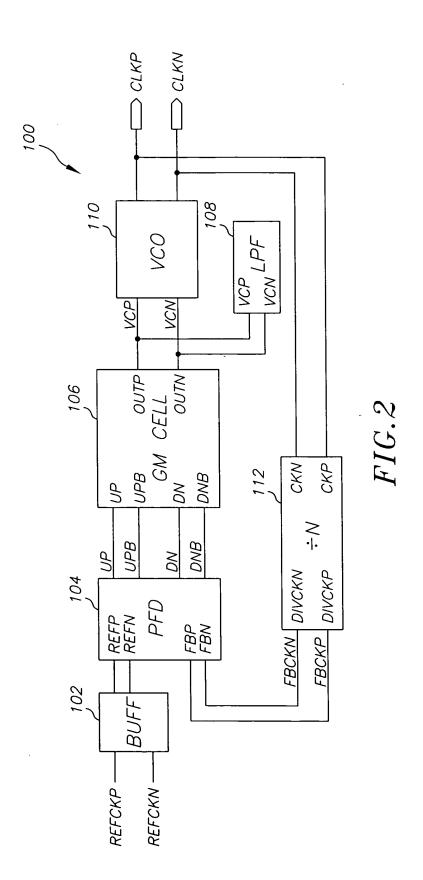
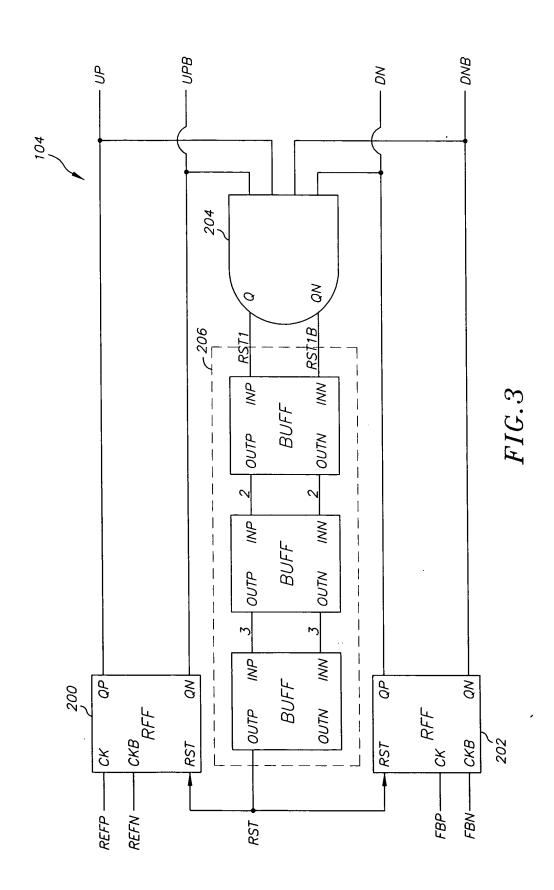
Attorney: Richard J. Paciulan Docket No.: 52118/RJP/B600 Inventor(s): Armond Hairapetian, et al. Title: FULLY DIFFERENTIAL CMOS PHASE-LOCKED LOOP Sheet 1 of 5



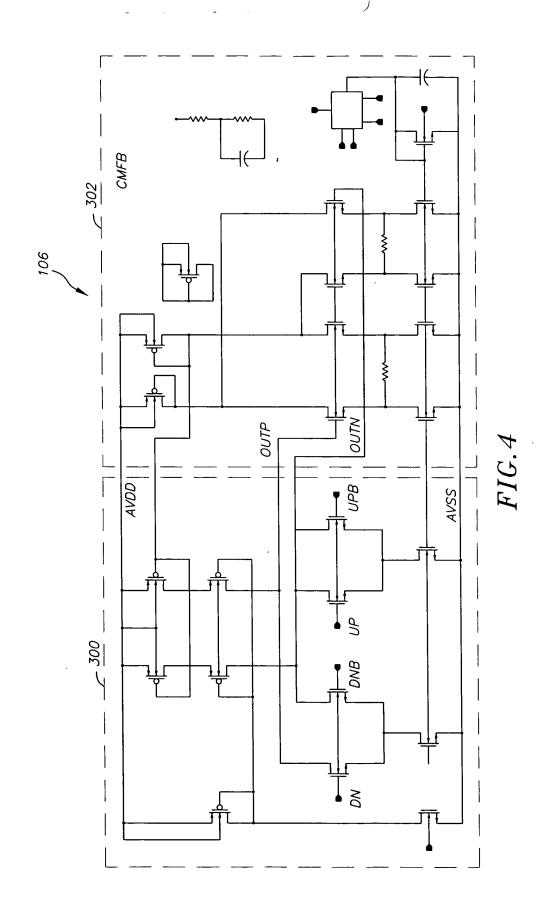
Attorney: Richard J. Paciulan
Docket No.: 52118/RJP/B600
Inventor(s): Armond Hairapetian, et al.
Title: FULLY DIFFERENTIAL CMOS PHASELOCKED LOOP
Sheet 2 of 5



Attorney: Richard J. Paciulan
Docket No.: 52118/RJP/B600
Inventor(s): Armond Hairapetian, et al.
Title: FULLY DIFFERENTIAL CMOS PHASELOCKED LOOP
Sheet 3 of 5



Attorney: Richard J. Paciulan
Docket No.: 52118/RJP/B600
Inventor(s): Armond Hairapetian, et al.
Title: FULLY DIFFERENTIAL CMOS PHASELOCKED LOOP
Sheet 4 of 5



Attorney: Richard J. Paciulan
Docket No.: 52118/RJP/B600
Inventor(s): Armond Hairapetian, et al.
Title: FULLY DIFFERENTIAL CMOS PHASELOCKED LOOP
Sheet 5 of 5

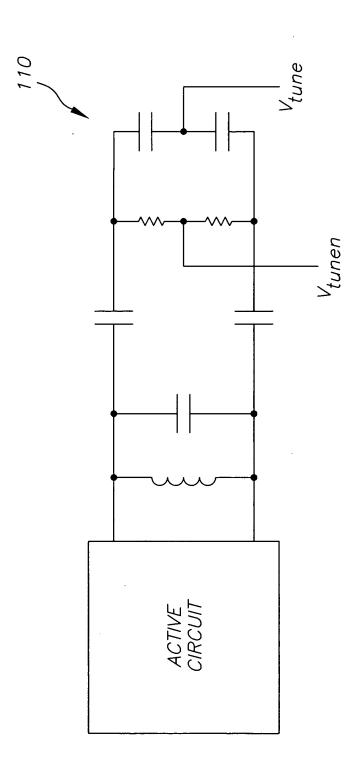


FIG.5